

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 7,639,651 B2

APPLICATION NO. : 10/596,403

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INVENTOR(S) : Di Lodovico, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

$BD_{IP} = (\text{req_slots} * SL * 8 + (PP - \text{req_slots}) * \lceil \log_2(RTT + N_{RG} - 1) \rceil * 100 / (PP * SL * 8)).$

In Column 11, Lines 10-11, delete “ $BD_{IP} = (\text{req_slots} * SL * 8 + (PP - \text{req_slots}) * \lceil \log_2(RTT + N_{RG} - 1) \rceil * 100 / (PP * SL * 8)).$ ”
and insert - - $BD_{IP} = (\text{req_slots} * SL * 8 + (PP - \text{req_slots}) * \lceil \log_2(RTT + N_{RG} - 1) \rceil * 100 / (PP * SL * 8)).$ - -, therefor.

In Column 12, Line 55, delete “ $q_{x,i}(t)$ ” and insert - - $q_{x,c,i}(t)$ - -, therefor.

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